



優潔光科技
UJ Light Technologies

SPECIFICATIONS FOR LCD MODULE

Model No:

UTF050E-KFN\$

UJ Light Technologies Corporation

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1. General Specification

Item	Specification	Unit
Screen Size(inch)	5.0"	
LCD Mode	a-Si TFT/ transmissive	
Display Mode	Normally Black	
Viewing Direction	ALL	O' Clock
Display Resolution	720*3(RGB)*1280	pixels
Outline Dimension	66.8(W)*121.3(H)*2.25(T)	mm
Active Area(A.A)	62.1(W)*110.4(H)	mm
Pixel pitch	28.75(H)*86.25(V)	mm
Pixel arrangement	RGB Vertical stripe	
Backlight	White LED-12	PCS
Driver IC	ILI9881C-04	
Interface	MIPI	

2. Absolute Maximum Rating

Item	Symbol	Conditions	Min.	Max.	Unit
Power Supply Voltage for LCD	VCC	GND=0	-0.3	6.6	V
	VGH	GND=0	-0.3	32	V
	VGL		-16	0.3	V
	VGH-VGL	GND=0	-0.3	32	V
Operating temperature	TOP		-20	70	°C
Storage temperature	TST		-30	80	°C

3. Electrical Characteristics

3.1 TFT LCD Electrical Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Common Voltage	VCOM	-	-1.53	-	V
Power Supply Voltage	VGH	10	18	20	V
	VGL	-10.5	-10	-9.5	V
	IOVCC	1.65	2.8	3.3	V
	VCC	2.6	2.8	3.3	V
Input Signal Voltage	VIH	0.7VCC	-	VCC	V
	VIL	GND	-	0.3VCC	V
Drive Current	VCC		35		mA

3.2 BackLight Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
LED Voltage	VF	22	24	26	V
LED Current	IF	-	60	-	mA
LED Luminance	IV	900	1000	-	cd/m ²
LED Uniformity	B	80	-	-	(%)
LED Life Time	-	20000	-	-	Hr

4. Interface Pin Function

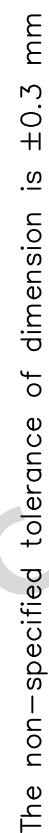
Pin NO.	Symbol	Description
1	GND	Power ground
2	VCC	Power supply (2.8V)
3	FMARK	Teraing effect output pin
4	GND	Power ground
5	IOVCC	Power supply (2.8V)
6	RESET	Global rest pin
7	GND	Power ground
8	LEDA1	LED Anode
9	LEDA2	LED Anode
10	GND	Power ground
11	LEDK1	LED Cathode
12	LEDK2	LED Cathode
13	GND	Power ground
14	LCM_ID	LCD Identify pin
15	LEDPWM	LCD Backlight Control pwm output pin
16-17	GND	Power ground
18	D3P	MIPI DSI differential data pin (data lane 3+)
19	D3N	MIPI DSI differential data pin (data lane 3-)
20	GND	Power ground
21	MESH2	No connection
22-23	GND	Power ground
24	D2P	MIPI DSI differential data pin (data lane 2+)
25	D2N	MIPI DSI differential data pin (data lane 2-)
26-27	GND	Power ground
28	MESH1	No connection
29	GND	Power ground
30	TP-GND-RING	No connection
28	SDA	Serial communication data input and output.
29	NC	Not connect
30	GND	Power ground
31	GND	Power ground
32	D0P	MIPI DSI differential data pin (data lane 0+)
33	D0N	MIPI DSI differential data pin (data lane 0-)
34	GND	Power ground
35	CLKP	MIPI DSI differential clock+ pin



36	CLKN	MIPI DSI differential clock- pin
37	GND	Power ground
38	D1P	MIPI DSI differential data pin (data lane 1+)
39	D1N	MIPI DSI differential data pin (data lane 1-)
40	GND	Power ground

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