優潔光科技
UJ Light Technologies

SPECIFICATIONS FOR LCD MODULE

Model No:

UTF101B-CBN$

UJ Light Technologies Corporation
5F, No. 38, Keya Rd., Daya Dist., Central Taiwan Science Park, Taichung City, 42881, Taiwan.

Web Site: https://www.ujlight.com/ **TEL:+886-4-25658689 FAX:+886-4-25658698**

1. General Specification

Item	Specification	Unit
Screen Size(inch)	10.1”	
Color	16.7M	
Display Resolution	1024*3(RGB)*600	pixels
Viewing Direction	6H	O’ Clock
Outline Dimension	235.0(W)*143.0(H)*2.8(T)	mm
Active Area(A.A)	222.72*(W)*125.28(H)	mm
Display Mode	Normally White	
Pixel Arrangement	RGB-stripe	
Surface Treatment	Anti-Glare,Hardness:3H	
Interface	LVDS	
Backlight	White LED*27 CHIP	pcs

2. Absolute Maximum Rating

Item	Symbol	Min.	Typ.	Max.	Unit
Power Supply Voltage	VGH	20	21	22	V
Power Supply Voltage	VGL	-9	-8	-7	V
Operating Temperature	TOP	-20	-	60	°C
Storage Temperature	TST	-30	-	70	°C

3. Electrical Characteristics

3.1 TFT LCD Electrical Characteristics

Item	Symbol	Min	Typ	Max	Unit
Power Supply Voltage	DVDD	3.0	3.3	3.6	V
Power Supply Voltage	AVDD	9.4	9.6	9.8	V
Input signal voltage	VCOM	3.3	3.8	4.3	V

3.2 BackLight Characeristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VF	8.0	9.0	10.5	V
Supply Current	IF	-	180	-	mA
Uniformity	B	80			%
Color	White				

4. Interface Pin Function

Pin NO.	Symbol	Description
1	VCOM	Common Voltage
2	VDD	Digital Power
3	VDD	Digital Power
4	NC	Not connect
5	RESET	Global rest pin
6	STBYB	Standby mode, Normally pulled high STBYB = “1”, normal operation STBYB = “0”, timing controller, source driver will turn off, all output are High-Z
7	GND	Power ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	LVDS differential data input
10	GND	Power ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	LVDS differential data input
13	GND	Power ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	LVDS differential data input
16	GND	Power ground
17	RXCLKIN-	- LVDS differential clock input
18	RXCLKIN+	+LVDS differential clock input
19	GND	Power ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	LVDS differential data input
22	GND	Power ground
23	NC	Not connect
24	NC	Not connect
25	GND	Power ground
26	NC	Not connect
27	DIMO	Backlight CABC controller signal output
28	SELB	6bit/8bit mode select
29	AVDD	Power for Analog Circuit
30	GND	Power ground
31	LED-	LED Cathode
32	LED-	LED Cathode
33	L/R	Horizontal inversion
34	U/D	Vertical inversion

35	VGL	Gate OFF Voltage
36	CABCEN1	CABC H/W enable
37	CABCEN0	CABC H/W enable
38	VGH	Gate ON Voltage
39	LED+	LED Anode
40	LED+	LED Anode

5. Drawing