



優潔光科技
UJ Light Technologies

SPECIFICATIONS FOR LCD MODULE

Model No:

UTF101C-CFN\$

UJ Light Technologies Corporation

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1. General Specification

Item	Specification	Unit
Screen Size(inch)	10.1"	
LCD Mode	a-Si TFT/transmissive	
Color	16.7M	
Display Resolution	1024*3(RGB)*600	pixels
Viewing Direction	All	O' Clock
Outline Dimension	235.0(H)*143.0(V)*4.5(D)	mm
Active Area(A.A)	222.72*(H)*125.28(V)	mm
Pixel pitch	0.0725(H) *0.2088(V)	mm
Display Mode	Normally White	
Pixel Arrangement	RGB-stripe	
Interface	LVDS	

2. Absolute Maximum Rating

Item	Symbol	Min.	Typ.	Max.	Unit
Digital Supply Voltage	VDD	-0.5	-	4.0	V
Power For Analog Circuit	AVDD	0.5	-	14.8	V
Operating Temperature	TOP	-20	-	70	°C
Storage Temperature	TST	-30	-	80	°C

3. Electrical Characteristics

3.1 TFT LCD Electrical Characteristics

Item	Symbol	Min	Typ	Max	Unit
Power Supply Voltage	VDD	3.0	3.3	3.6	V
Power For Analog Circuit	AVDD	7.0	9.6	13.5	V
TFT Gate ON Voltage	VGH	-	18	-	V
TFT Gate OFF Voltage	VGL	-	-6.0	-	V
Common Electrode Voltage	VCOM	-	4.25	-	V

3.2 BackLight Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VF	16.2	18.0	19.8	V
Supply Current	IF	-	140	-	mA
Uniformity	B	-	80	-	%
Color	White				

4. Interface Pin Function

Pin NO.	Symbol	Description
1	GND	Ground
2	AVDD	Power For Analog Circuit
3	VDD	Power Supply Input Voltage
4	GND	Ground
5	VCOM	TFT Common Electrode Voltage
6	VDD	Power Supply Input Voltage
7	GND	Ground
8-14	V14 - V8	External gamma voltage pin floating
15	GND	Ground
16	VDD	Power Supply Input Voltage
17	GND	Ground
18	RXIN3+	LVDS D3+ differential data input
19	RXIN3-	LVDS D3- differential data input
20	GND	Ground
21	RXCLKIN+	LVDS CLK+ differential clock input
22	RXCLKIN-	LVDS CLK- differential clock input
23	GND	Ground
24	RXIN2+	LVDS D2+ differential data input
25	RXIN2-	LVDS D2- differential data input
26	GND	Ground
27	RXIN1+	LVDS D1+ differential data input
28	RXIN1-	LVDS D1- differential data input
29	GND	Ground
30	RXIN0+	LVDS D0+ differential data input
31	RXIN0-	LVDS D0- differential data input
32-33	GND	Ground
34	GRB	RESET Pin
35	STBYB	STBYB=1 All the functions are on(default) STBYB=0 TCON and source driver are off and all output are GND
36	SHLR	Horizontal scan direction control pin.
37	VDD	Power Supply Input Voltage
38	UPDN	Vertical scan direction control pin
39	GND	Ground
40	AVDD	Power For Analog Circuit



41	VCOM	TFT Common Electrode Voltage
42	DITH	Dithering function enable control DITH=1 enable internal Dithering function DITH=0 disable internal Dithering function
43	GND	Ground
44	VDD	Power Supply Input Voltage
45	GND	Ground
46-52	V7-V1	External gamma voltage pin floating
53	GND	Ground
54	VDD	Power Supply Input Voltage
55	SELB	LVDS 6/8 bit input select SELB=1 LVDS 8 bit SELB=0 LVDS 6 bit
56	VGH	TFT Gate ON Voltage
57	VDD	Power Supply Input Voltage
58	VGL	TFT Gate OFF Voltage
59	GND	Ground
60	BIST	Not connected



5. Drawing

