



優潔光科技
UJ Light Technologies

SPECIFICATIONS FOR LCD MODULE

Model No:

UTF035C-AFN\$

UJ Light Technologies Corporation

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1. General Specification

Parameter	Value	Unit
LCD Mode	a-Si TFT/transmissive	-
Screen Size(inch)	3.5"	
Display Mode	Normally Black	-
Viewing Direction	80/80/80/80	-
Color	16.7M	-
Display Resolution	320*3(RGB)*240	pixels
Outline Dimension	76.9(W) *63.9(H) *3.2(T)	mm
Active Area(A.A)	70.08(H) * 52.56(V)	mm
Pixel Arrangement	RGB-stripe	-
Driver IC	ST7272A	
Interface	RGB	

2. Absolute Maximum Rating

Item	Symbol	Min.	Max.	Unit
Digital Supply Voltage	VDD	-0.3	4.0	V
Logic Signal Input Level	V _i	-0.3	VDD+0.3	
Operating Temperature	TOP	-20	70	°C
Storage Temperature	TST	-30	80	°C

3. Electrical Characteristics

3.1 TFT LCD Electrical Characteristics

Item	Symbol	Min	Typ	Max	Unit
Power Supply Voltage	VGH	13	15	16.5	V
	VGL	-7	-10	-11	V
	VDD	3.0	3.3	3.3	V
Operation Current	IOC	-	20	-	mA
Logic Input Voltage	V _{IH}	0.7VDDI	-	VDD	V
	V _{IL}	DGND	-	0.3VDDI	V
Logic Output Voltage	V _{OH}	VDDI-0.4	-	VDDI	V
	V _{OL}	DGND	-	DGND+0.4	V

3.2 BackLight Characeristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply Voltage	V _F	Only Backlight	17.5	18.5	20.0	V
Supply Current	I _F		20			mA
Average Brightness	I _V	Backlight Current I _F =20mA	300	350	-	cd/m ²
CIE Color Coordinate	X	Backlight Current I _F =20mA	0.245	0.295	0.345	-
	Y		0.296	0.346	0.396	
Uniformity	B	Backlight Current I _F =20mA	75	-	-	(%)
Color	White					

4. Interface Pin Function

FPC connector is used for electronics interface.

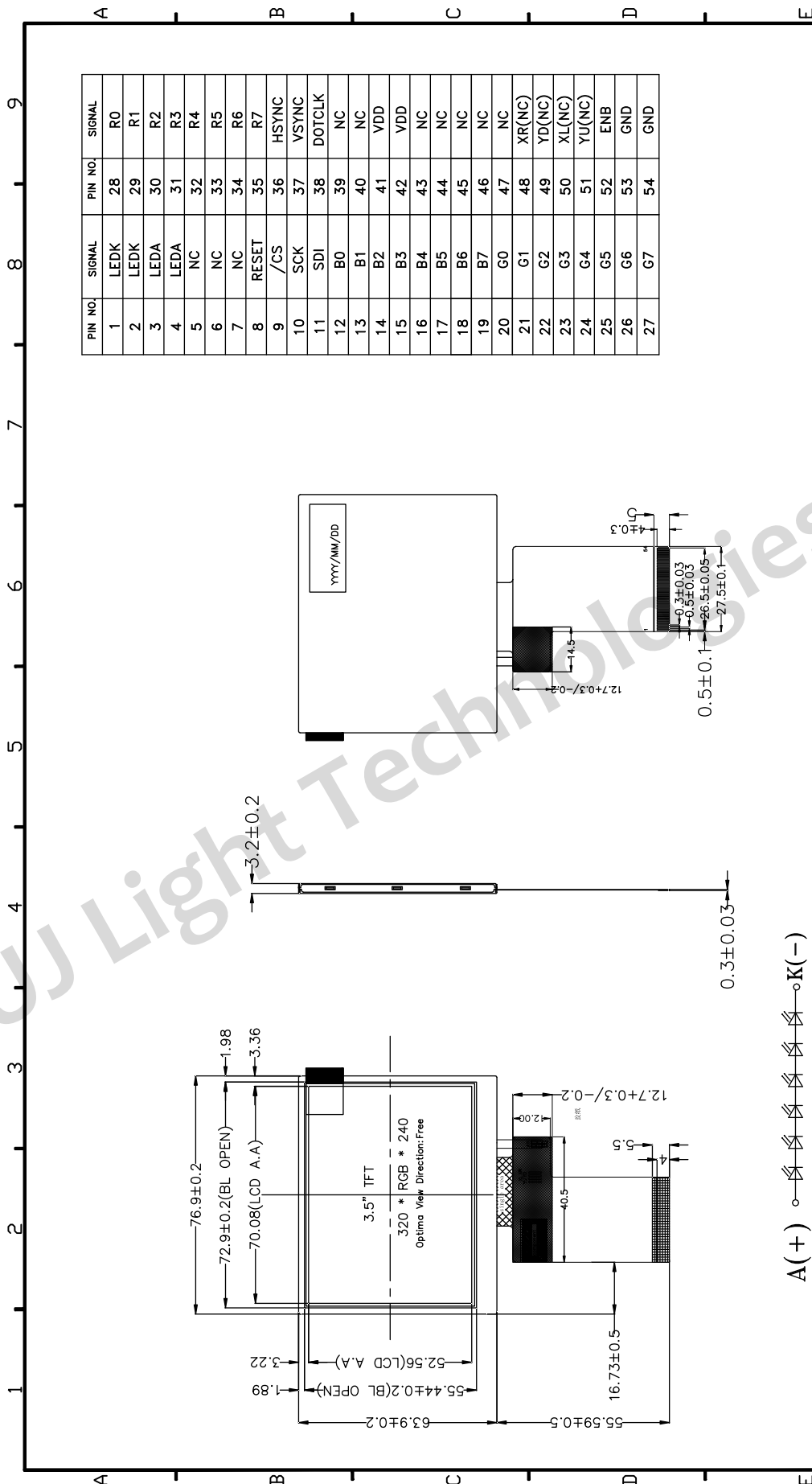
The recommended model is FH19SC-54S-0.5SH (51) manufactured by HIROSE.

Pin NO.	Symbol	Description
1	LEDK	Power for LED backlight cathode
2	LEDK	Power for LED backlight cathode
3	LEDA	Power for LED backlight anode
4	LEDA	Power for LED backlight anode
5-7	NC	No Connect
8	RESET	RESET PIN
9	/CS	Chip select pin of serial interface
10	SCK	Clock pin of serial interface
11	SDI	Data input pin of serial interface
12-19	B0-B7	Blue data
20-27	G0-G7	Green data
28-35	R0-R7	Blue data
36	HSYNC	Horizontal sync signal
37	VSYNC	Vertical sync signal
38	DOTCLK	Pixel clock
39-40	NC	No Connect
41	VDD	Power voltage
42	VDD	Power voltage
43-51	NC	No Connect
52	ENB	Data enable
53-54	GND	Power ground

Note:

1. The mode control (SEL2) not use, it can't control CCIR601 interface. If not use CCIR601, it can floating.
2. For digital RGB input data format, both SYNC mode and DE+SYNC mode are supported. If DE signal is fixed now, SYNC mode is used. Otherwise, DE+SYNC mode is used. Suggest used SYNC mode. Suggest the DE signal usually pull low.
3. Usually pull high.
4. IF select serial RGB or CCIR601/656 input mode is selected, only DX0-DX7 used, and the other short to GND, only selected serial RGB、CCIR601/656 interface, DX BUS will enable. Digital input mode DX0 is LSB and DX7 is MSB.
5. Control the input data format

5. Drawing



CIRCUIT DIAGRAM

The non-specified tolerance of dimension is ± 0.3 mm

[illegible]