



● Feature

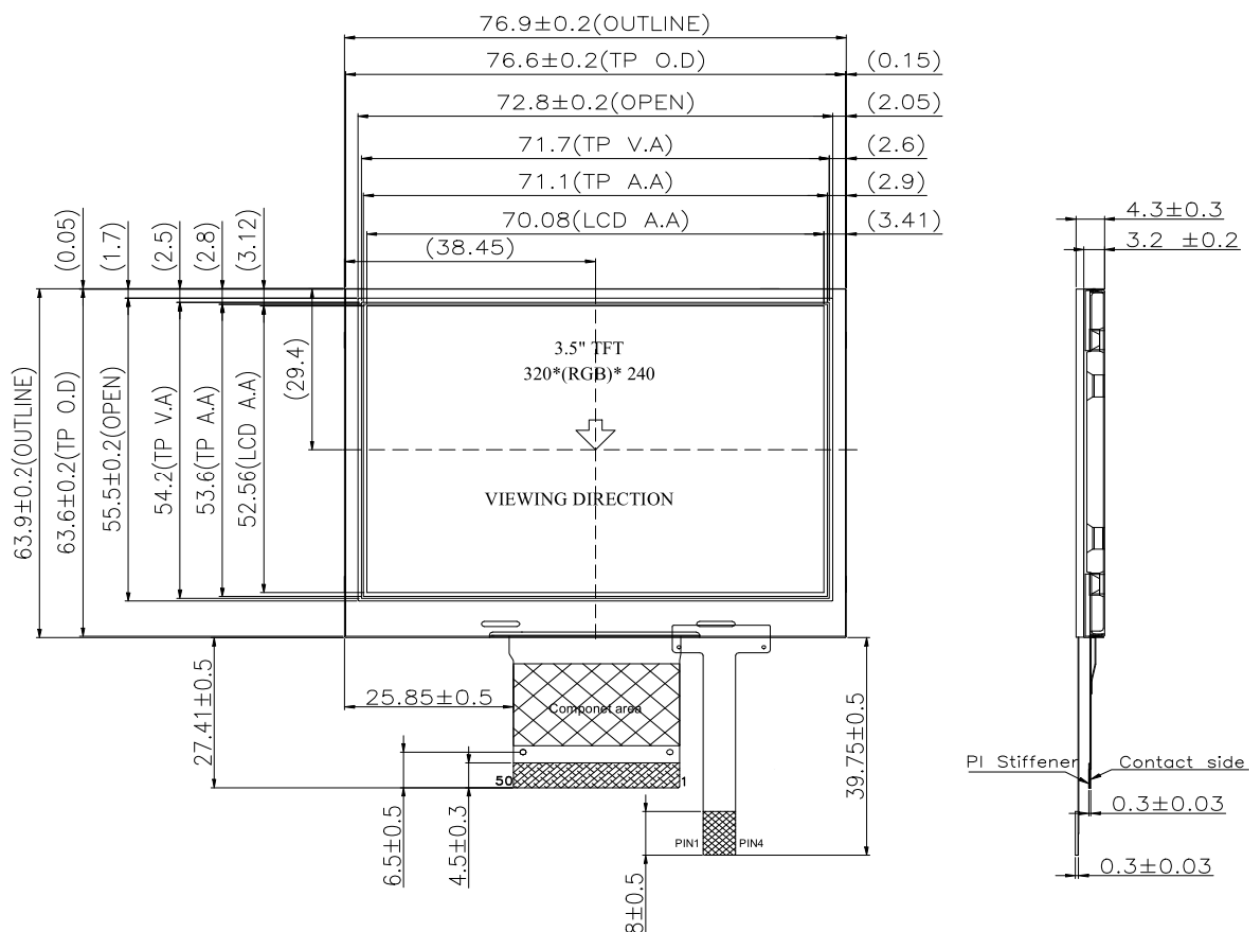
1. 3.5" TFTLCD
2. Resolution:320*240
3. Display Type:TFT/Transmissive/Positive
4. Interface Type:18BIT RGB+SPI
5. Drive IC:SSD2119
6. Surface Luminance:500cd/m²
7. Top:-20°C~70°C
8. Support Resistive Touch Panel

● Mechanical Data

1. Module(WxHxT)(mm):76.9*63.9*4.3
2. Active Area(mm):70.08*52.56
3. LED Numbers:6 LEDs



● Mechanical Drawing



● Interface Pin Function

Pin No.	Symbol	Description
1~2	VCI	Power supply for analog
3	VSS	Ground.
4	VDDIO	Voltage input pin for logic I/O
5	VSS	Ground.
6	RESB	System reset pin. - An active low pulse at this pin will reset the IC, Connect to VDDIO in normal operation
7	DC/SDC (RS)	A register select signal. Low: select an index or status register, High: select a control register.
8	E/ $\overline{\text{RD}}$	6800-system : E (enable signal) 8080-system : RD (read strobe signal) Serial mode : Not used and should be connected to VDDIO or Vss
9	WR	8080-system : WR (write strobe signal)
10	CS	CS : Chip select pin
11	SCL	Serial clock input
12	SDO	Data output pin in serial interface
13	SDI	Data input pin in serial interface
14	WSYNC	Ram Write Synchronization output -Leave it OPEN when not used
15~32	DB17~DB0	Data bus.
33	VSS	Ground.
34	DOTCLK	Dot-clock signal and oscillator source.
35	HSYNC	Line Synchronization input
36	VSYN	Frame/Ram Write Synchronization input
37	OE	Display enable pin from controller.
38	VSS	Ground.
39	PS0	Refer of Table1
40	PS1	
41	PS2	
42	PS3	
43	VSS	Ground.
44~47	NC	Not Connection
48	VSS	Ground.
49	LEDK	Cathode of LED backlight.
50	LEDA	Anode of LED backlight.

● Electrical Characteristics

Item	Symbol	Min	Typ.	Max	Unit	Applicable terminal
Supply Voltage for Analog	VCI	3.0	3.3	3.6	V	
Supply Voltage for Logic	VDDIO	3.0	3.3	3.6	V	
Input Voltage	V _{IL}	GND	-	0.3VCI	V	
	V _{IH}	0.7 VCI	-	VCI		
Input leakage Current	I _{LKG}	-1		1	μA	